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Enclosed for filing is a patent application under 37 CFR 1.53(b) of:

Inventor:

Alexandria, VA 22313-1450

Je-Min Park

For:

METHOD FOR FABRICATING SEMICONDUCTOR DEVICE

BY FORMING DAMASCENE INTERCONNECTIONS

Applicant requests FIG. 8A to be published with the application.

## **Enclosures:**

Specification (pages 1-11); claims (pages 12-15); abstract (page 16)

EIGHTEEN sheet(s) of FORMAL drawings (comprising 24 figures)

Certified copy of Priority Document No. 2002-47588, filed August 12, 2002

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CLAIMS AS FILED						
For	Number Filed	Number Extra	Rate		Basic Fee \$750	
Total Claims	17-20	0	x \$ 18	=	\$0.00	
Independent Claims	2-3	`0	x \$ 84	=	\$0.00	
TOTAL FILING FEE		L	l		\$750.00	

$\boxtimes$	Combined Declaration and Power of	Attorney
$\boxtimes$	Newly executed (original or copy)	
	Assignment with cover sheet	
	Assignee Name and Address:	Samsung Electronics Co., Ltd.
		Suwon-city, Kyungki-do
	•	Republic of Korea
$\boxtimes$	PTO Form 2038 authorizing credit card payment of \$750.00 (filing fee) is encl Any deficiency or overpayment should be charged or credited to deposit account number 13-1703.	

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Respectfully submitted,

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